

ZeroPoint Technologies Teams with OCP Composable Memory Systems TCO Sub-Team Member to Showcase Inline Hardware Memory Compression

ZeroPoint Technologies and Seagate Technology LLC to Showcase CXL Memory Tier Capacity Expansion Demonstration and Highlight Progress Toward Lower \$/GB Memory and TCO Reduction

GOTHENBURG, Sweden, October 13, 2025 -- This week, at the OCP Global Summit in San Jose, California, ZeroPoint Technologies and Seagate Technology LLC, a global, mass capacity data storage leader, will unveil a real-time demonstration showcasing the power of hardware-accelerated memory compression within a CXL memory tier. The demonstration falls under the two companies' work as part of the Open Compute Project's (OCP) Composable Memory Systems (CMS) TCO Sub-Team initiative and utilizes representative workloads to validate the significant cost, capacity, and performance benefits inherent to ZeroPoint's hardware-accelerated, memory optimization solutions by incorporating Seagate's expertise of the demands of hyperscale workloads.

"Since the start of 2025, we've seen a clear evolution regarding how customers and partners look at hardware-accelerated memory optimization in their product pipeline," said Angelos Arelakis, Co-Founder and CTO, ZeroPoint Technologies. "There is a clear paradigm shift happening in real time – the transition from hardware-accelerated memory technologies being viewed as a nice to have differentiator, to increasingly being viewed as a necessary core technology to meet the TCO reduction demands of the hyperscalers."

The demonstration will show the significant advantages of this approach, enabling companies utilizing CXL-connected memory and serving hyperscale workloads to realize:

- **Breakthrough Efficiency:** Up to 2.25× compression (1.85× avg) with cache-line compression technology and <1% CXL traffic — matching hyperscaler-grade production metrics.
- **No Performance Trade-offs:** Latency-sensitive workloads maintain SLA-level tail latency with 90%+ page-cache hit rates.
- **Significant Cost Savings:** Up to 44% TCO savings in high-density deployments while clearing hyperscaler adoption targets.

"The Open Compute Project's mission is to make scalable infrastructure more efficient, open, and cost-effective," added Arelakis. "Inline compression within composable memory systems is a key enabler of that vision — and what we are demonstrating is not a vendor-specific solution, but a community-driven benchmark of what the next generation of memory architectures can deliver."

The Open Compute community has already validated the need for compressed CXL memory tiers in their published specifications and ZeroPoint is dedicated to moving the industry from discussing theoretical benefits to observing practical real-world implementations with measurable performance gains and clear economic impact.

To see the demonstration live, visit OCP Global Summit, Oct. 14-16 at the San Jose Convention Center Expo Hall in the Innovation Village. More information can be found here [2025 OCP Global Summit » Open Compute Project](#).

About ZeroPoint Technologies AB

ZeroPoint Technologies is the leading provider of hardware-accelerated memory optimization solutions for a variety of use cases, ranging from enterprise and hyperscale datacenter implementations to consumer devices. Based in Gothenburg, Sweden, ZeroPoint has developed an extensive portfolio of intellectual property. The company was founded by Professor Per Stenström and Dr. Angelos Arelakis, with the vision to deliver the most efficient memory compression available, across the memory hierarchy, in real-time, based on state-of-the-art research. For more information, visit <https://www.zeropoint-tech.com/>.

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