

Sivers Semiconductors and Chalmers University of Technology granted 4 MSEK to collaborate in research and development of new generation power amplifiers

Sivers Semiconductors AB (publ) ("Sivers") today announces that its business unit, Sivers Wireless, and the Department of Microtechnology and Nanoscience at Chalmers University of Technology ("Chalmers") will collaborate in research and development of a new generation of power amplifiers. The project targets a global market with a large growth potential among traditional broadband providers, in addition to new market segments made possible by Artificial Intelligence (AI) and Machine Learning (ML).

The project aims to make today's integrated radio circuits for license-free 5G broadband more competitive, energy-efficient and cost-effective by applying research results from high performance, low-cost and high-volume CMOS technology, while contributing to world class research.

The project's objective is to conduct research, develop, implement and characterize a new generation of adaptive power amplifiers with even better performance than today. Sivers Wireless and the Department of Microtechnology and Nanoscience at Chalmers contribute with complementary strengths in 5G power amplifier design, market, and applications.

"We are excited to work with Chalmers in this project and leverage their expertise in adaptable power-amplifier technology for electronically steerable antennas. The collaboration will help us boost the performance and efficiency of the next generation Sivers Wireless 5G beamforming transceivers," says Erik Öjefors, CTO of Sivers Wireless.

"We very much look forward to start this exciting collaboration with Sivers Wireless, as one of the most innovative companies in our field. By taking advantage of their unique expertise in high performance advanced CMOS design and future wireless system needs, we will be able to focus our research on the most relevant and timely questions and challenges." says Christian Fager, Professor and Head of the Microwave Electronics Laboratory at the Department of Microtechnology and Nanoscience, Chalmers.

Sivers and Chalmers have been granted 4 MSEK to run this project during two years with kick-off in September 2022. Sivers' part of the grant is 2 MSEK. The work is carried out within the "Strategic innovation program Smarter Electronics System" - a joint effort by Vinnova, Formas and Energimyndigheten (*Energy Agency*).



The purpose of the investment is to create conditions for Swedish industry's international competitiveness and sustainable solutions to global societal challenges.

For further information on the program: <https://www.smartareelektroniksystem.se/utlysningar/>

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Sivers Semiconductors AB is a leading and internationally recognized technology company that supplies ICs and integrated modules through its two business areas Wireless and Photonics. Wireless develops mmWave products for advanced 5G systems for data and telecommunications networks and satellite communication. The portfolio includes RF transceivers, beamforming front end ICs, integrated mmwave antennas, repeaters, and software algorithms for optimum mmWave RF performance. Photonics develops and manufactures semiconductor based optical products for optical fiber networks, sensors and optical fiber communications (Li-Fi). The company is listed on Nasdaq Stockholm under SIVE. The head office is located in Kista, Sweden. For more information: <http://www.sivers-semiconductors.com>