

## Sivers IMA's Chip paper selected best industry showcase at IEEE RFIC symposium

Sivers IMA Holding AB (Nasdaq First North: SIVE) today announces that its WiGig chip paper has been selected the number one industry showcase at IEEE Radio Frequency Integrated Circuit (RFIC) symposium, in competition with major semiconductor companies like Intel, Qualcomm, Samsung, Infineon and others. This happened at the event ceremony Sunday evening local time in Philadelphia. The IEEE RFIC symposium is the world premier RFIC conference focused on RFIC technology.

IEEE selected the winning paper among the industry contributions and it is based on Sivers IMA research and development of a world class WiGig (802.11ad) RFIC for data and telecom infrastructure applications. The paper is called, "A 57-71 GHz Beamforming SiGe Transceiver for 802.11ad-Based Fixed Wireless Access".

"A month ago we were very proud to announce that our paper was among top ten industry showcases for this event. And we were honoured to be selected among some of the giants in the RFIC industry, where the top 10 papers came from Texas Instruments, Intel, Northrop Grumman, Huawei, Samsung, Infineon and Global Foundries. Last night we were selected as number one among these companies. This is of course a massive stamp of approval that we are doing WiGig Chipsets that are considered being state of the art and first-rate also among world class solutions in the industry", comments Anders Storm, CEO of Sivers IMA Holding AB.

"It is very encouraging when an objective and well-respected organisation such as the IEEE give you such an award, and needless to say, our team has done an exceptional job!", says Mats Carlsson, CTO of Sivers IMA

The paper will be presented at the following time and place:

Conference: 2018 IEEE Radio Frequency Integrated Circuit (RFIC) symposium  
Venue: Technical Sessions, PA Convention Center, in Philadelphia, Pennsylvania, USA.  
Date: Tuesday 12 June 2018, 10:10 - 11:50  
Session: RTu1C-5:

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